



Atty. Dkt. No. 016891-0862

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Kenji YAMAMOTO  
Title: MASTER SLICE  
SEMICONDUCTOR  
INTEGRATED CIRCUIT  
Appl. No.: 10/718,515  
Filing Date: 11/24/2003  
Examiner: Lin, Sun J.  
Art Unit: 2825

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<u>EV70037385416</u> (Express Mail Label Number)	<u>MARCH 3, 2004</u> (Date of Deposit)
<u>Ruthic VALERO</u> (Printed Name)	
<u>[Signature]</u> (Signature)	

**AMENDMENT AND REPLY UNDER 37 CFR 1.111**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This communication is responsive to the Non-Final Office Action dated October 3, 2005, concerning the above-referenced patent application.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this document.

**Remarks/Arguments** begin on page 7 of this document.

Please amend the application as follows: